

FIG. 1

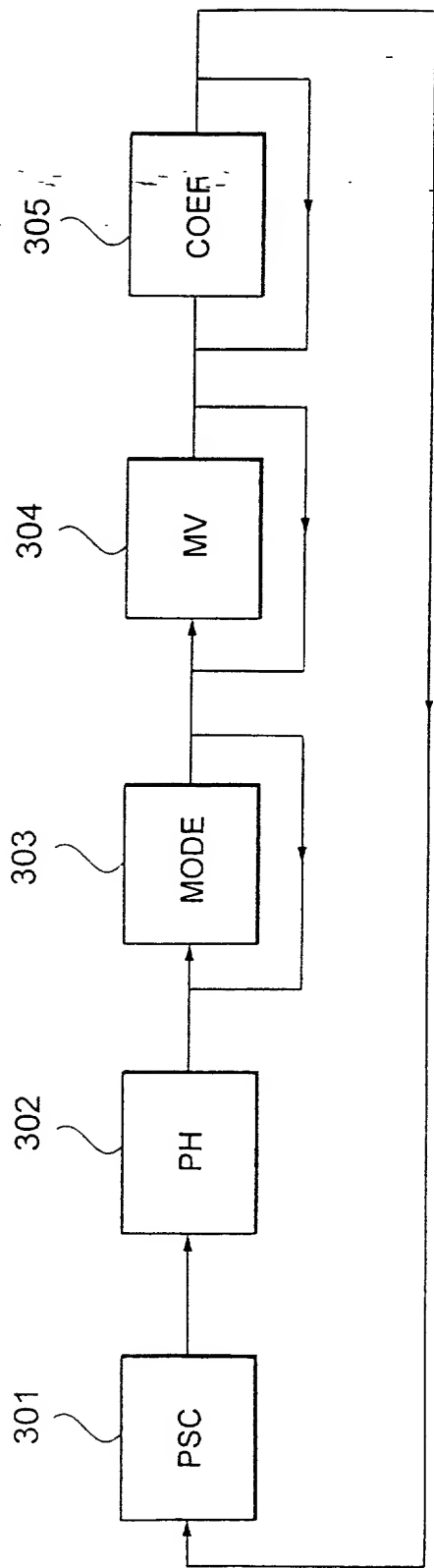


FIG. 2

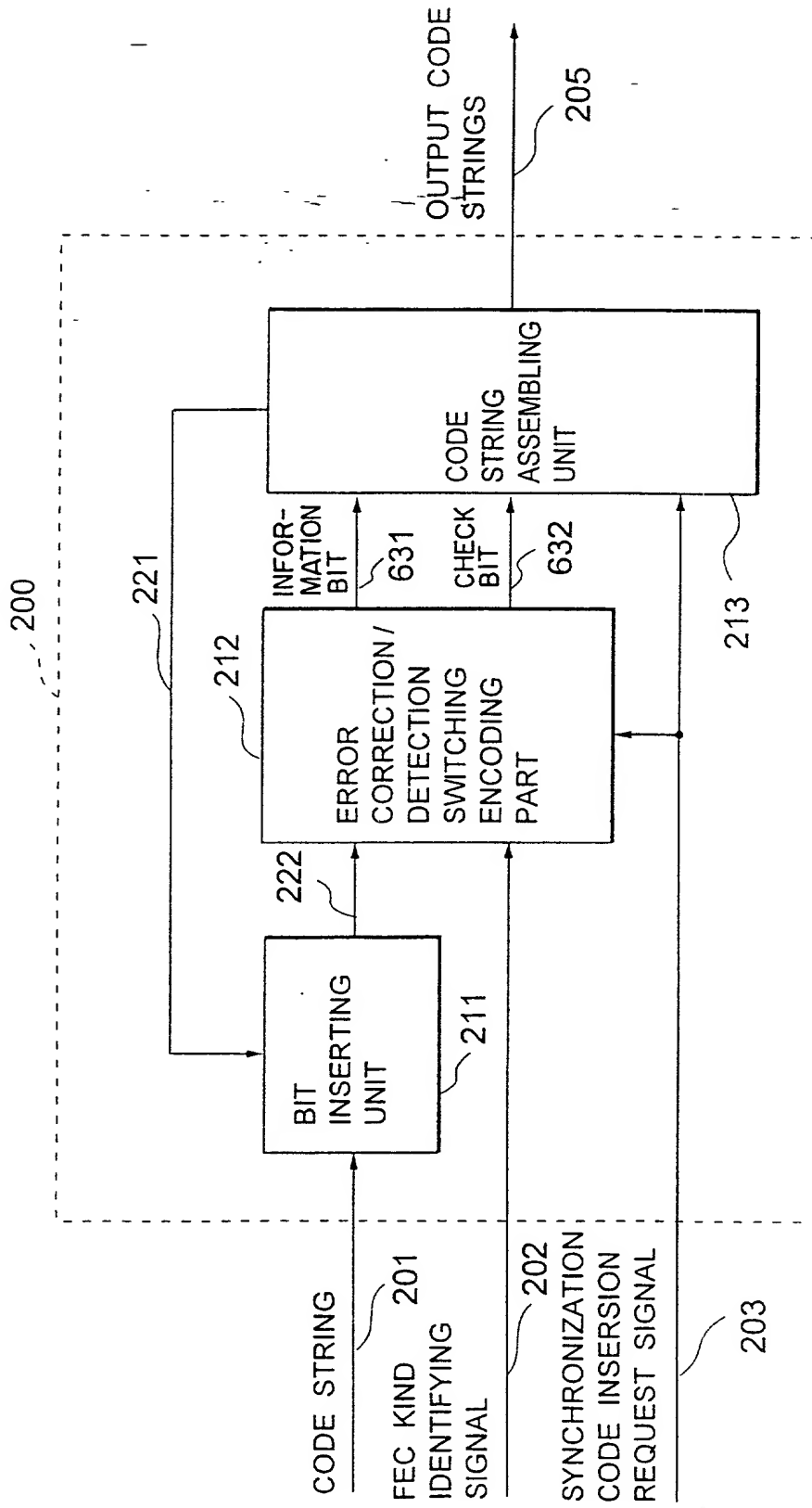


FIG. 3

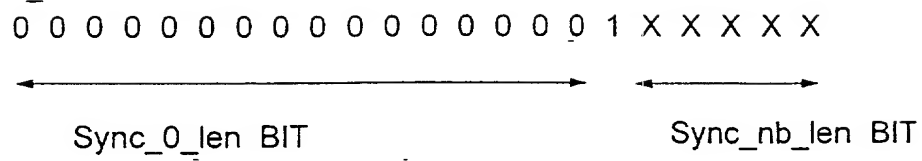


FIG.5

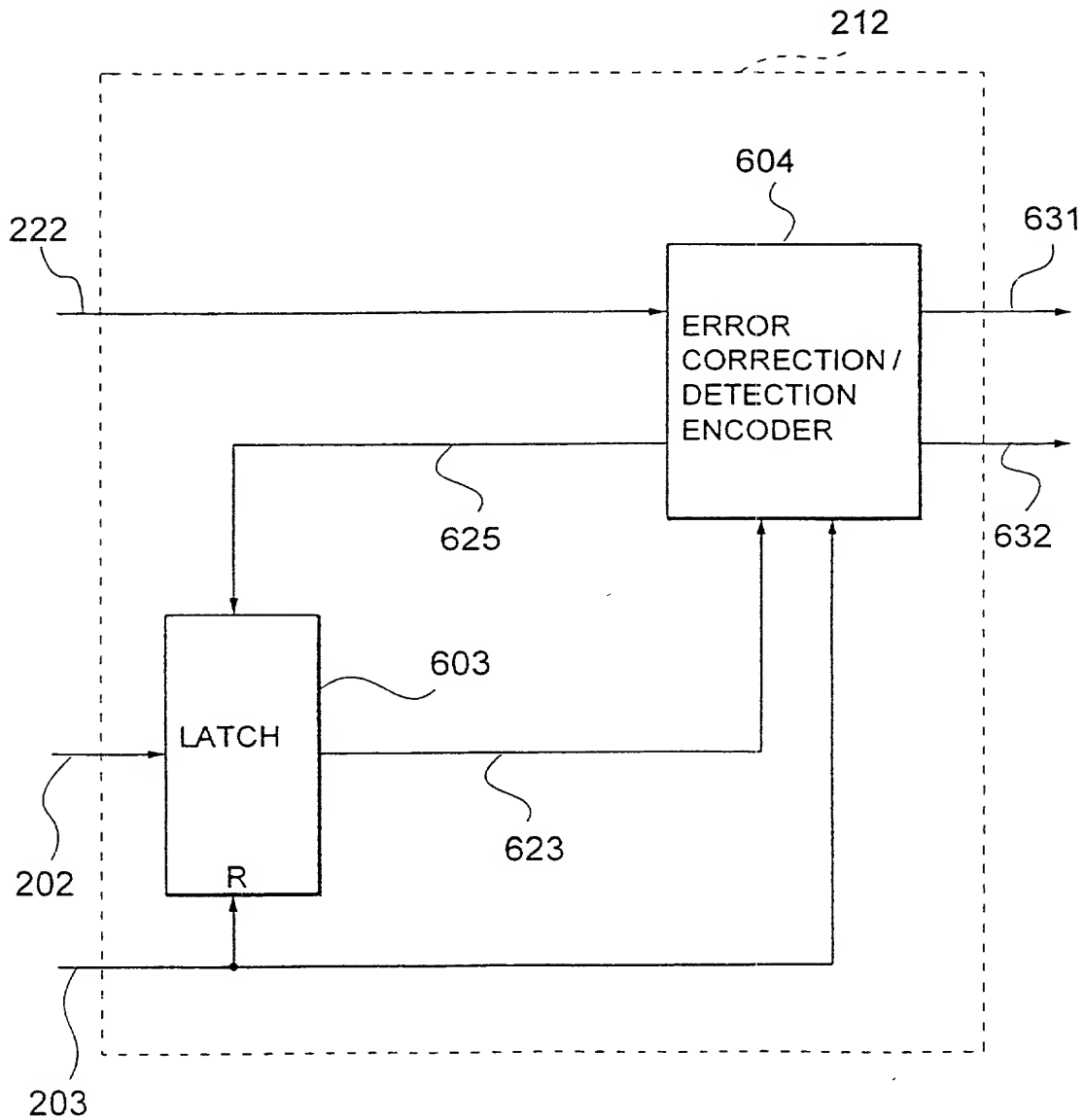


FIG.6

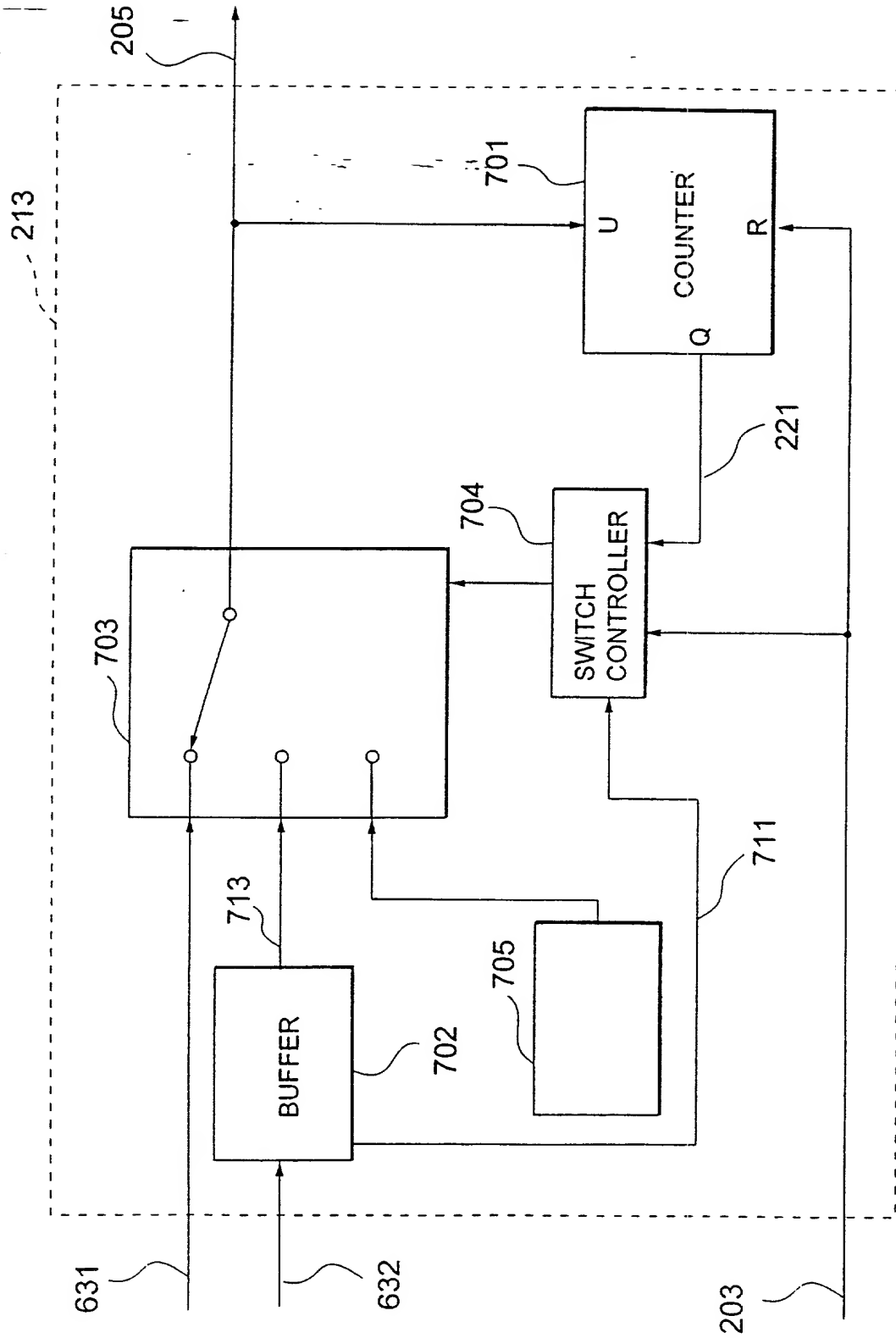


FIG. 7

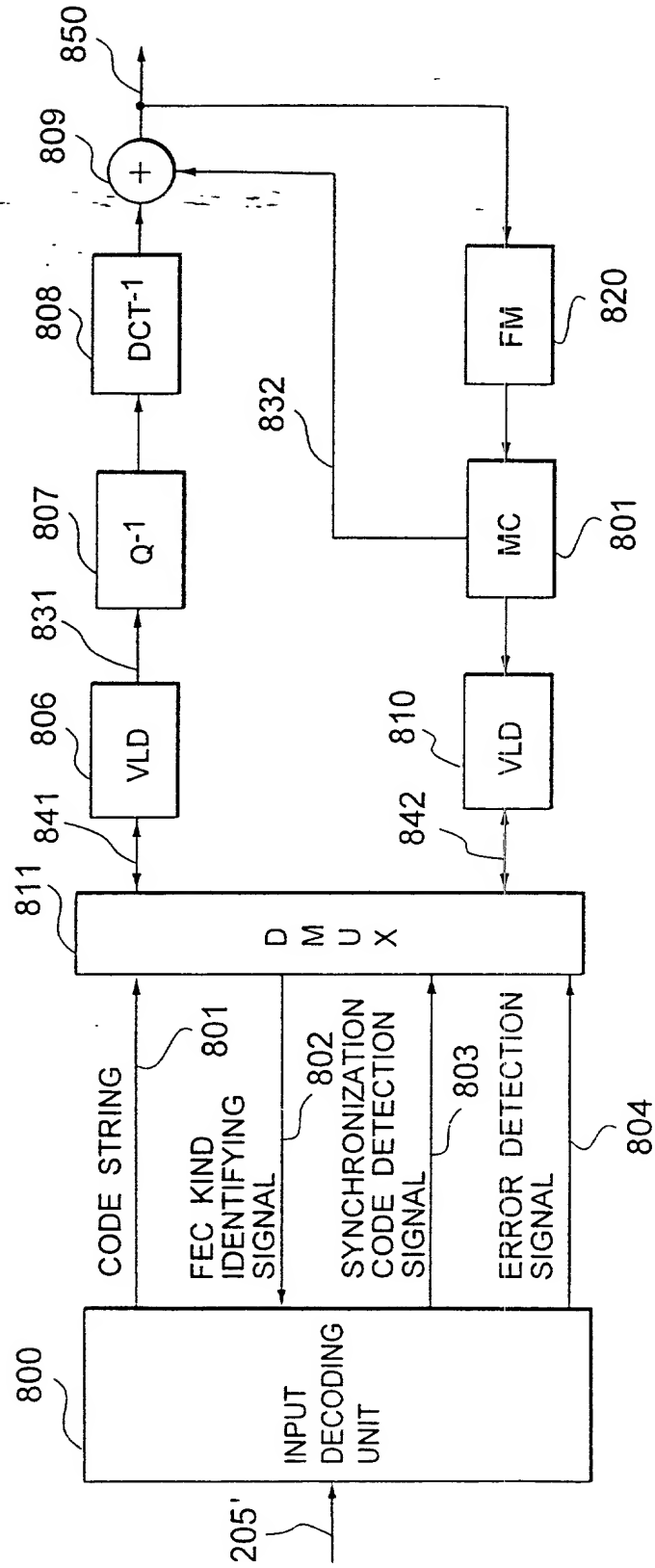


FIG. 8

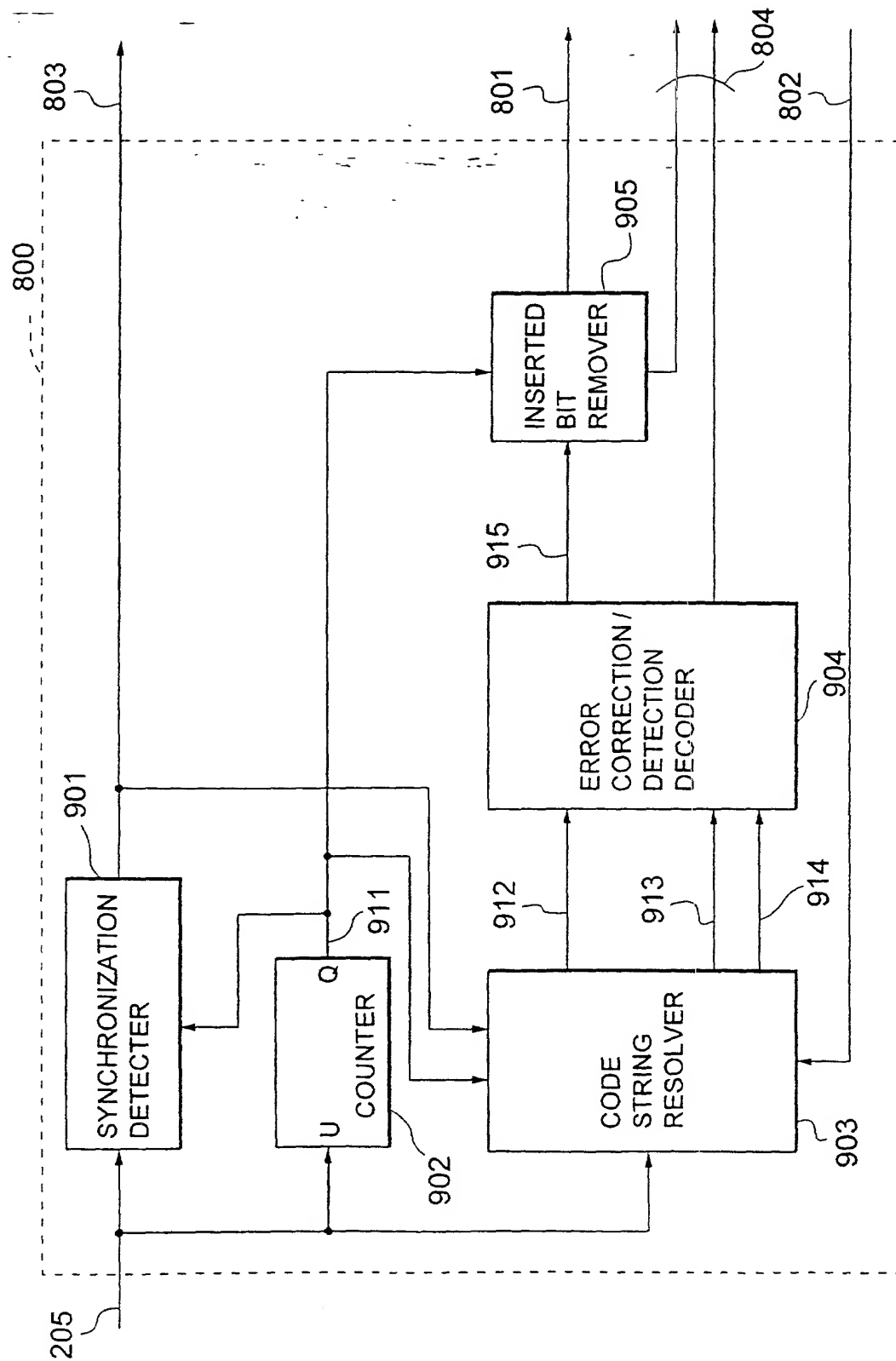


FIG. 9

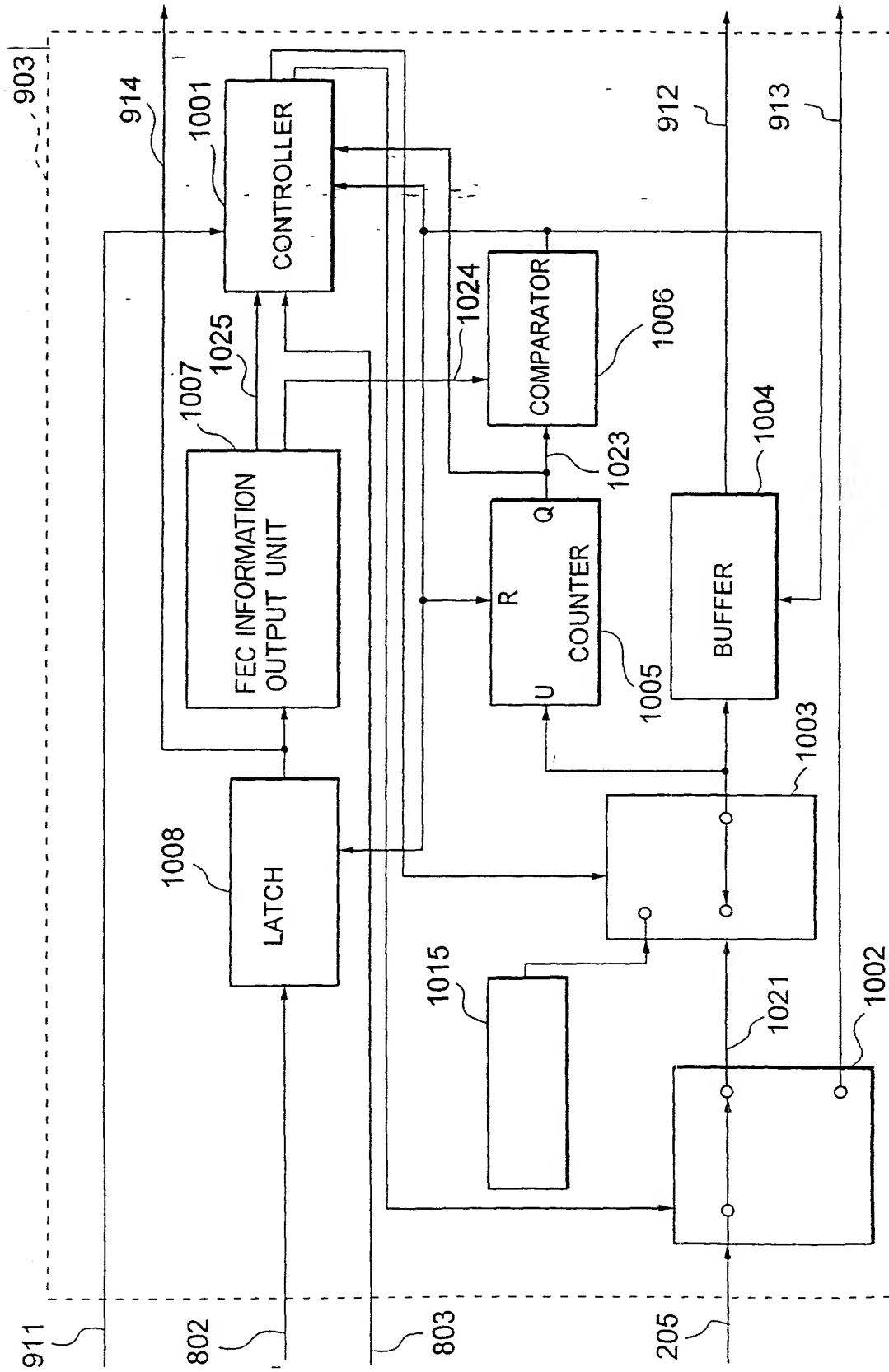


FIG. 10

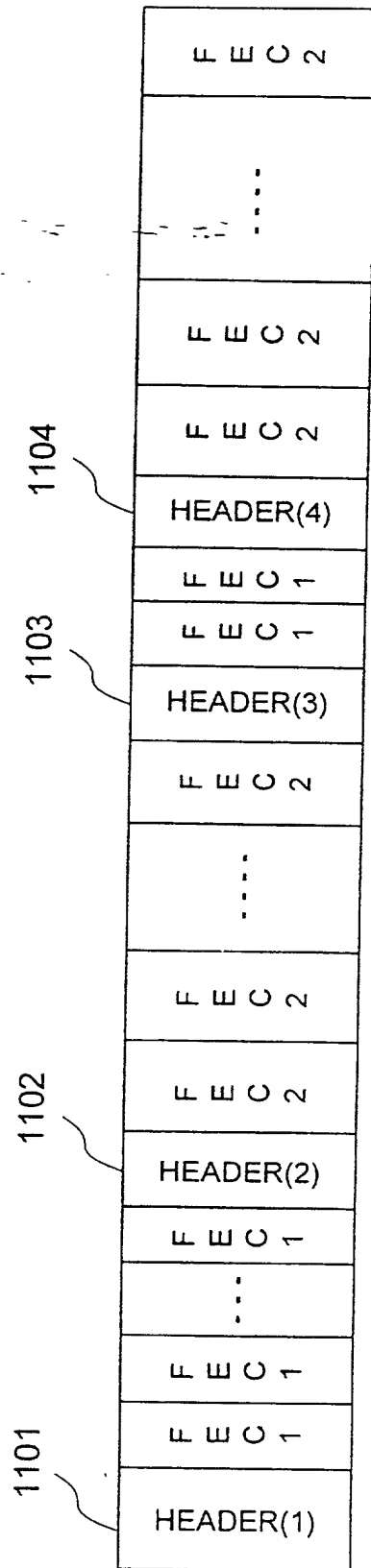
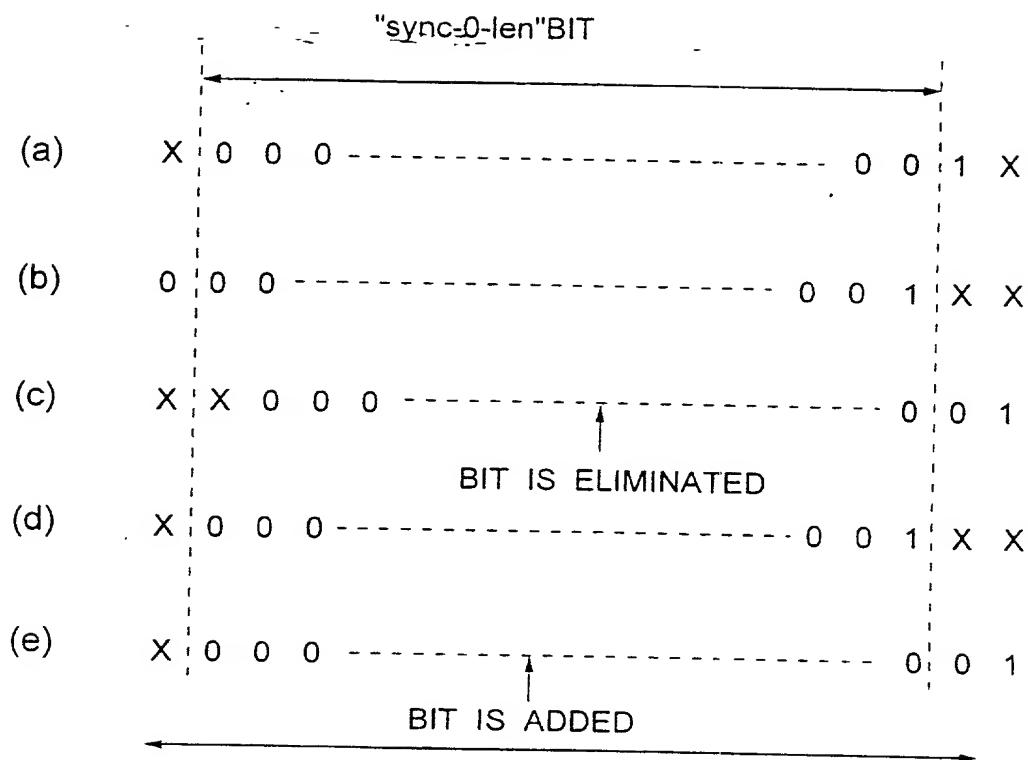


FIG. 11



SYNCHRONIZATION CODE DETECTION

WINDOW OF "sync-0-len+2"BIT

FIG.12

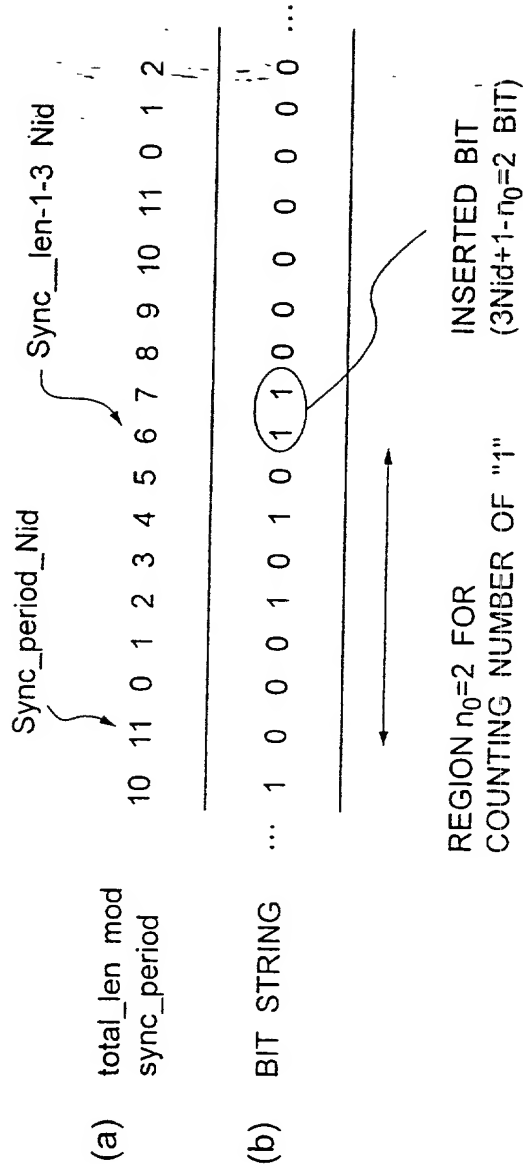


FIG. 13

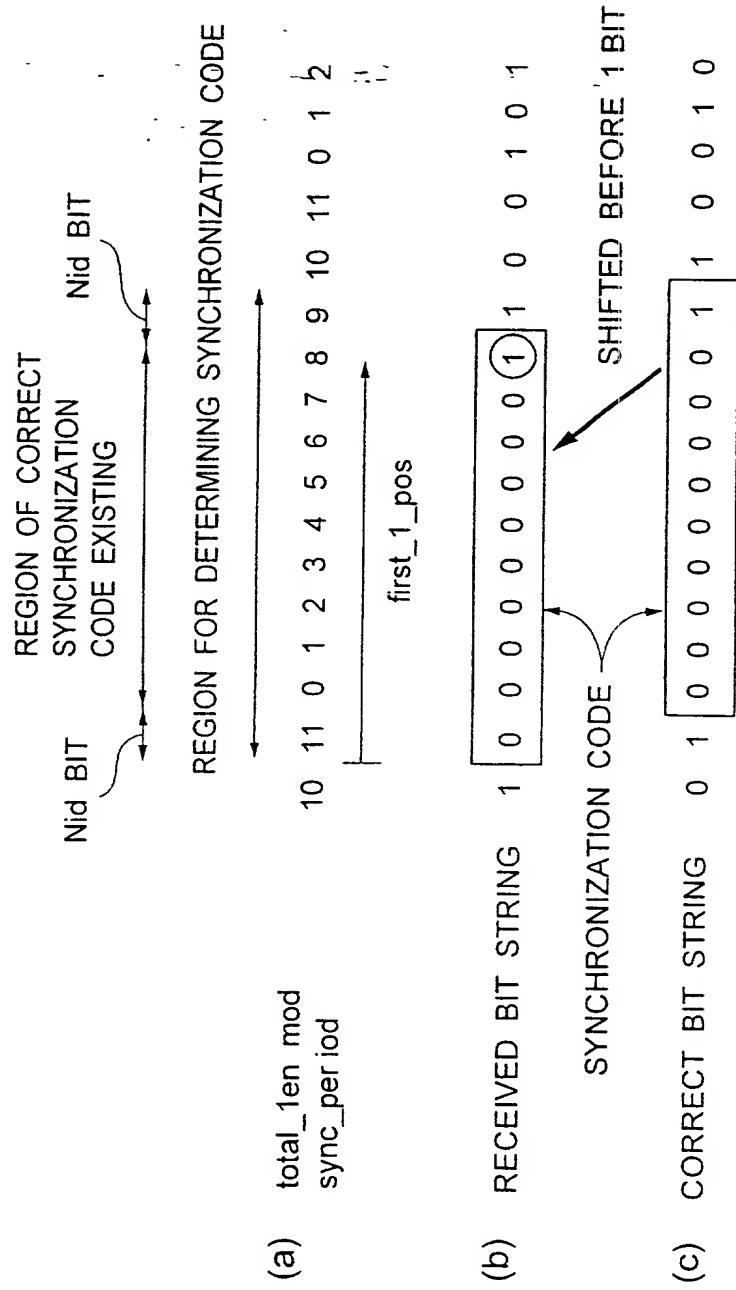


FIG. 14



FIG. 17

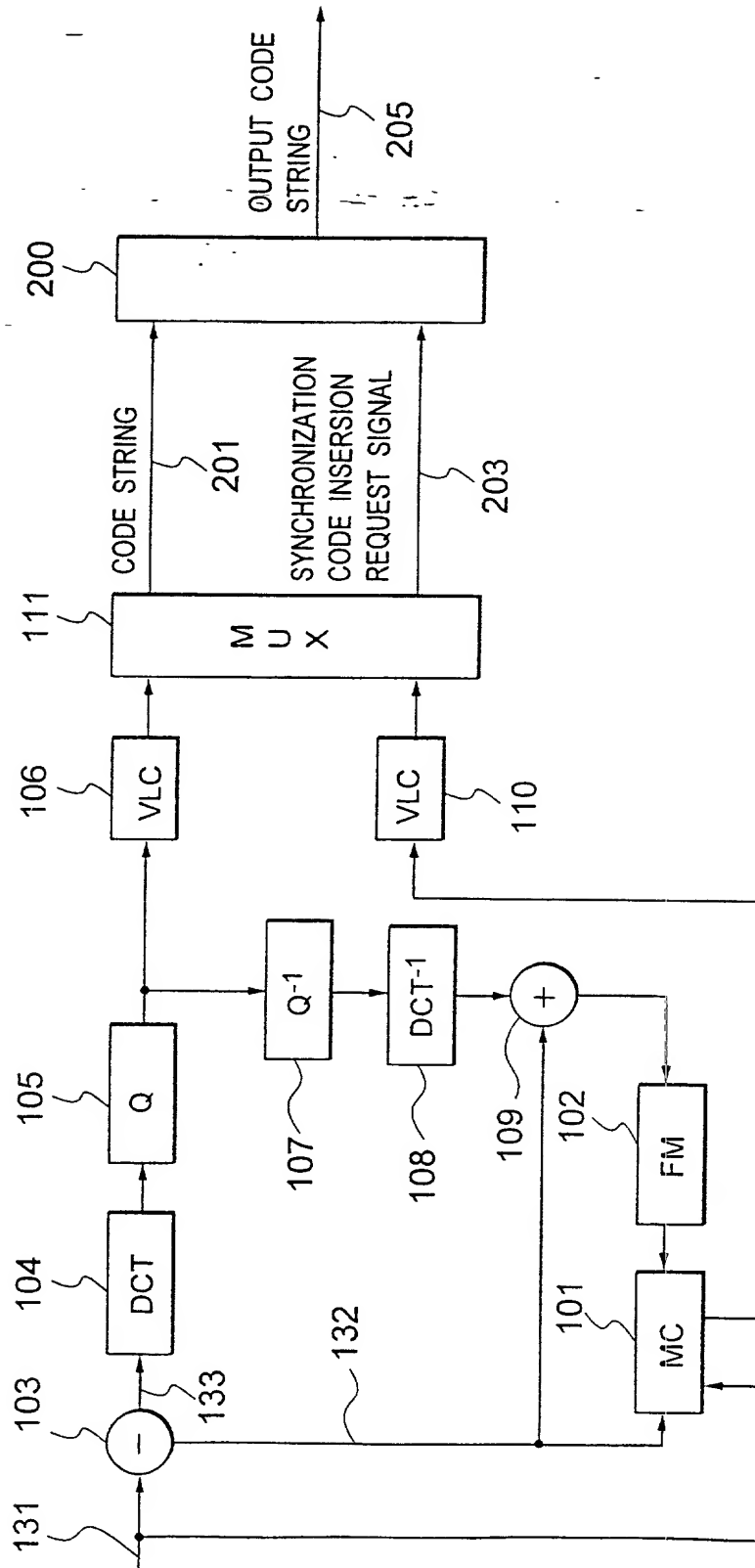


FIG. 18

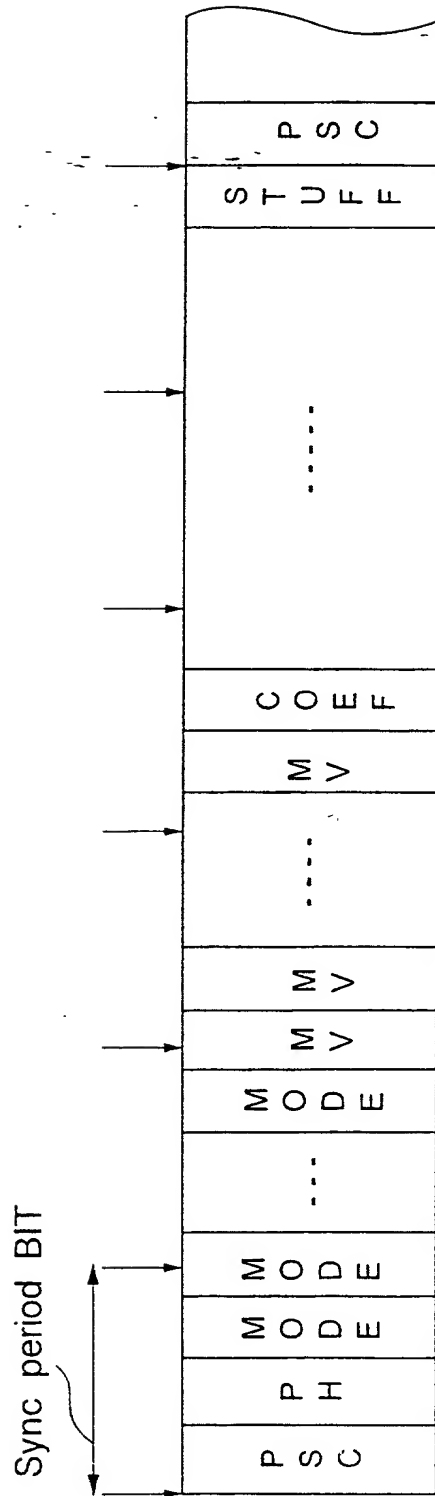


FIG. 20

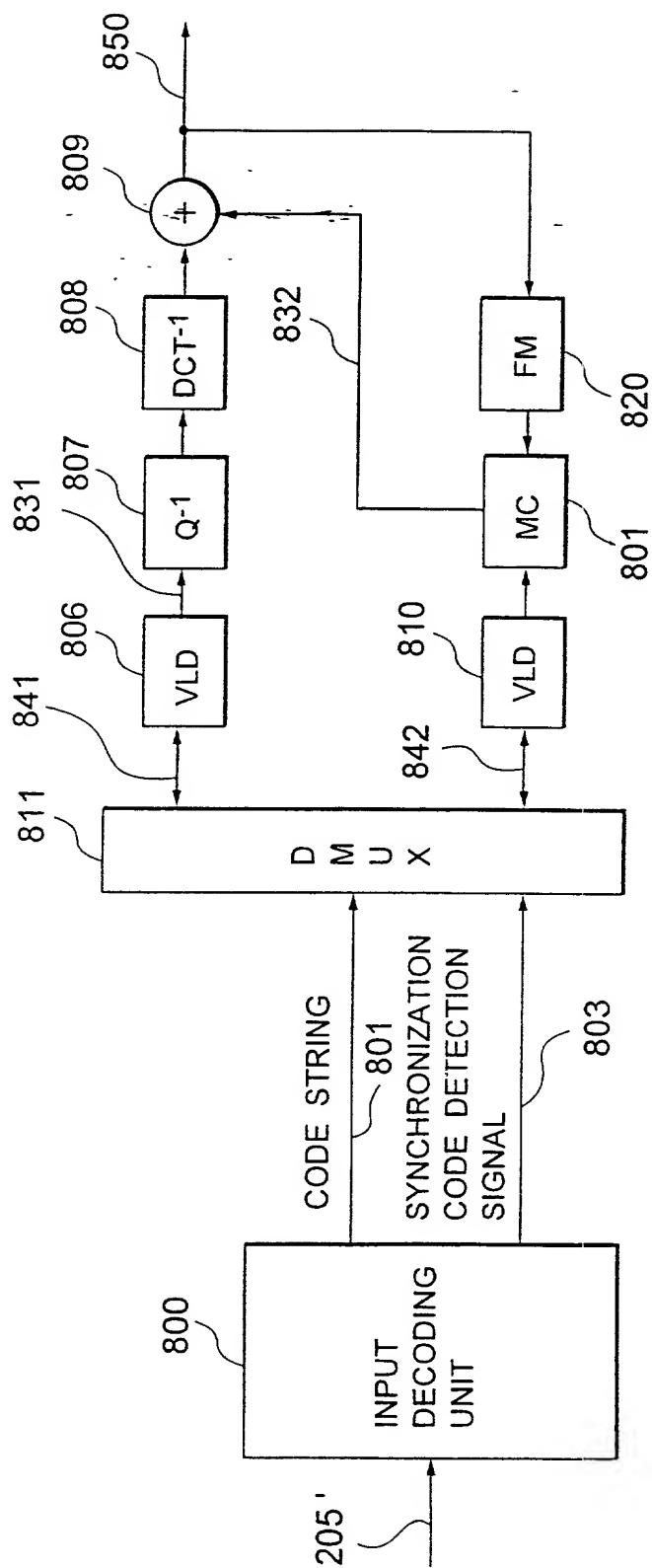


FIG. 21

FIG. 22

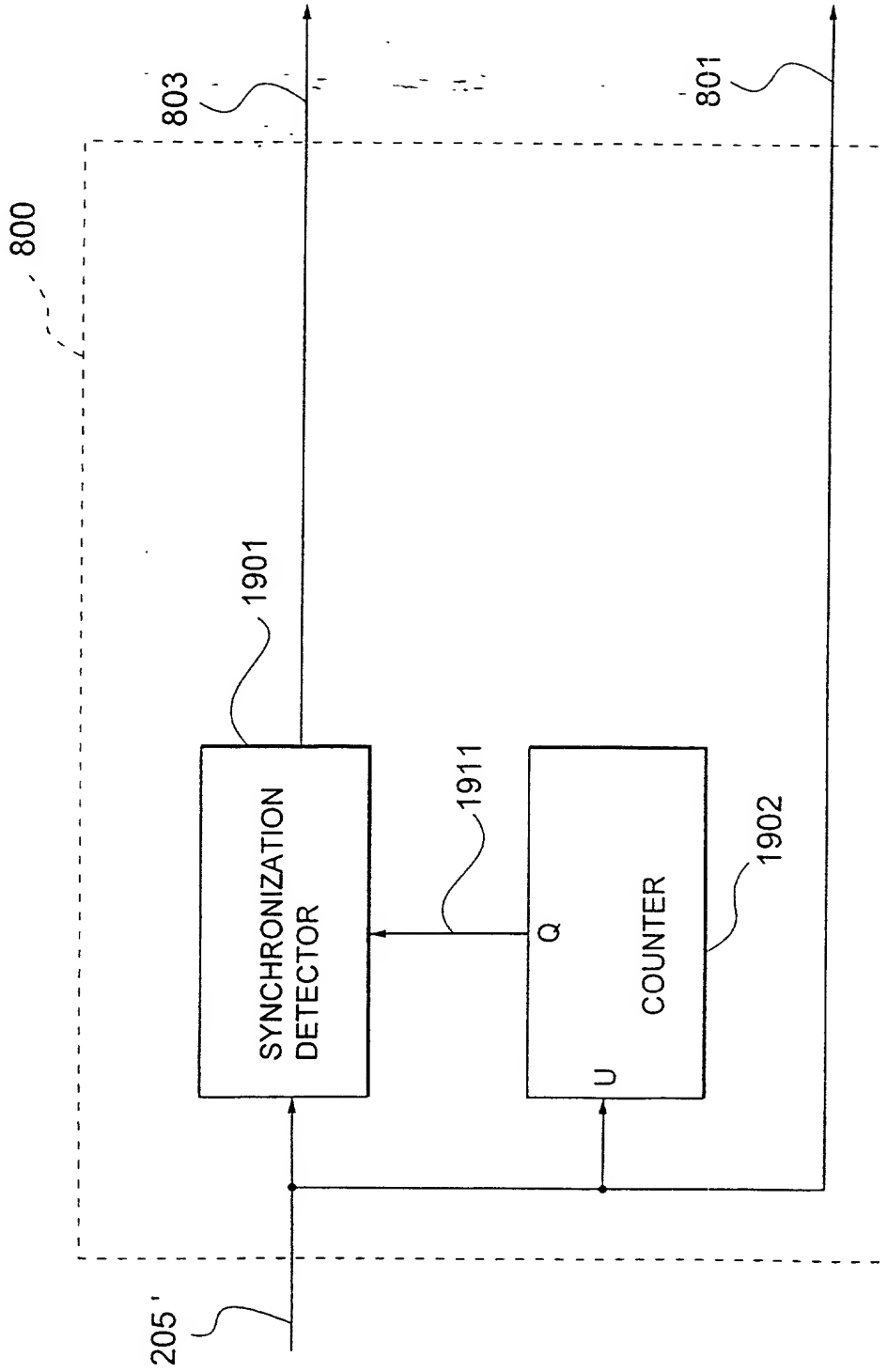


FIG. 22

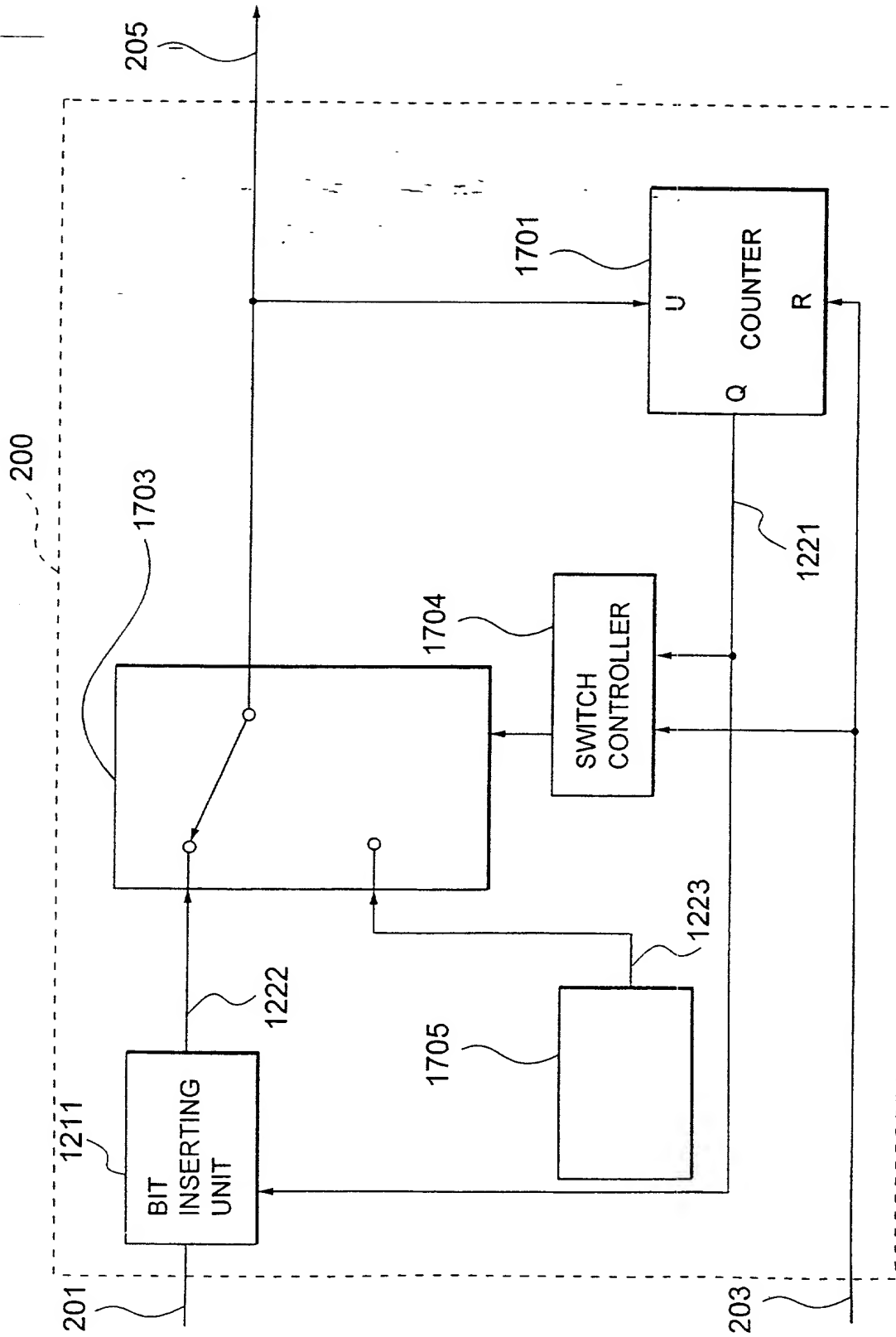


FIG. 23

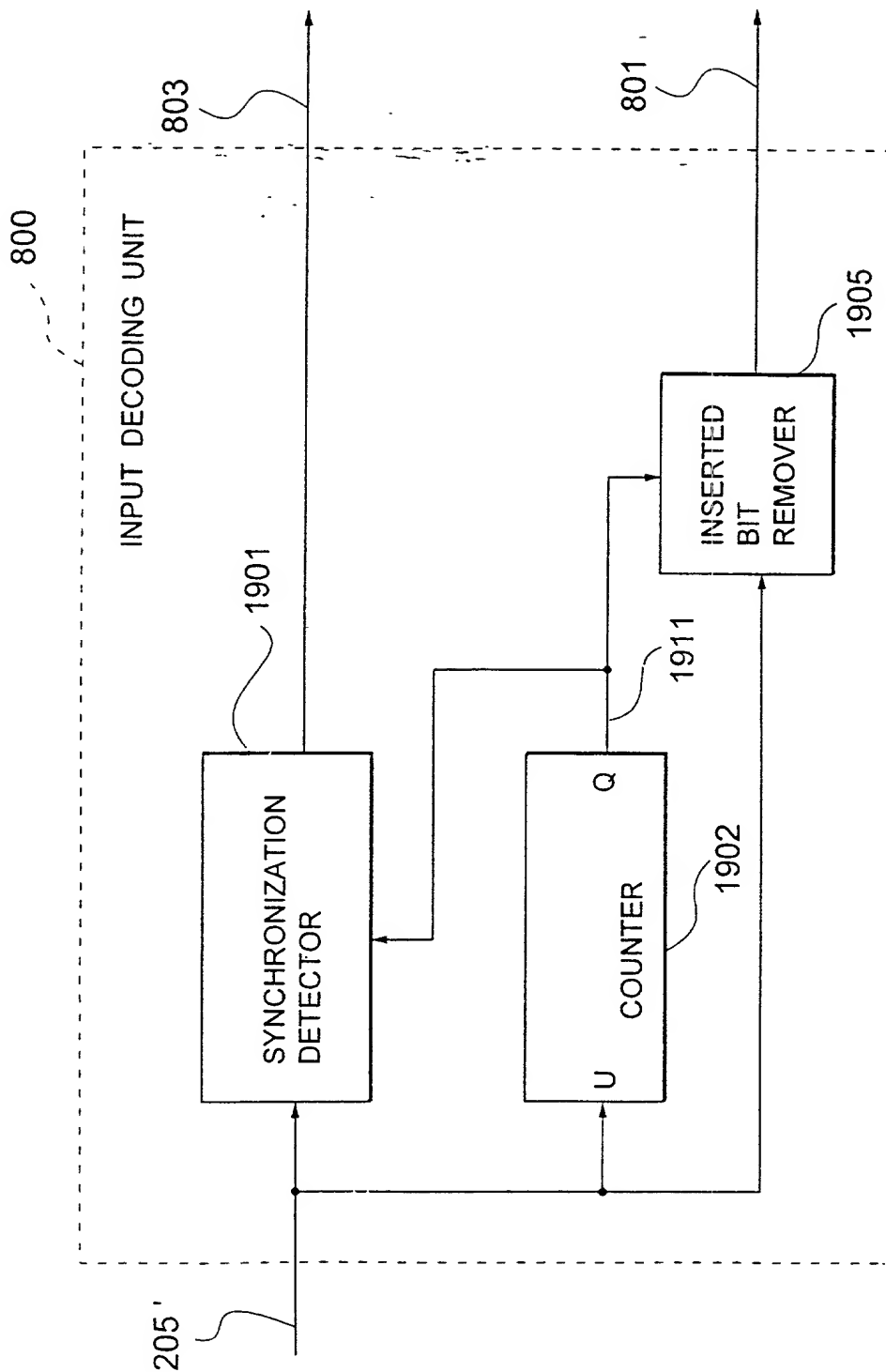
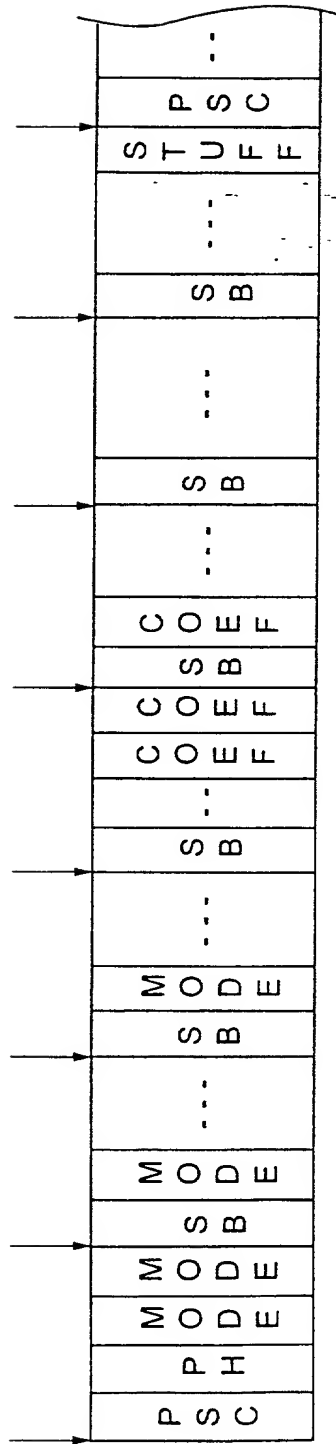
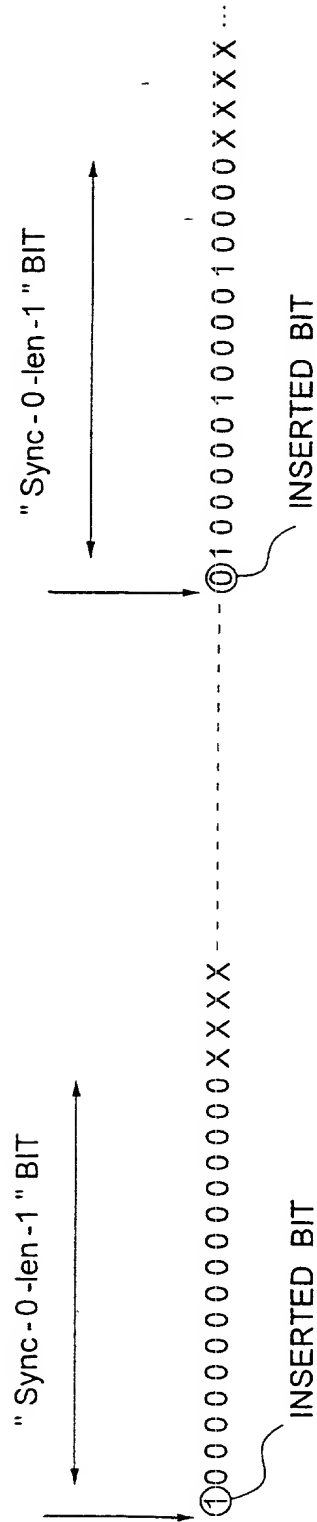


FIG. 24



(a)



(9)

FIG. 25

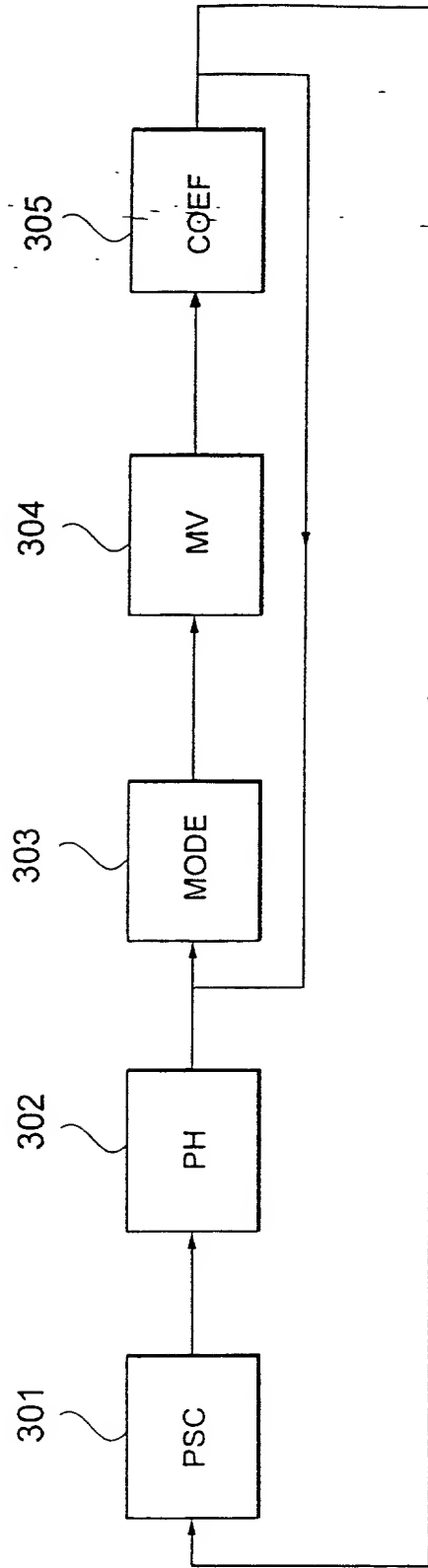
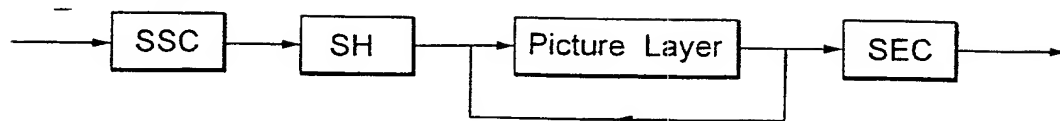


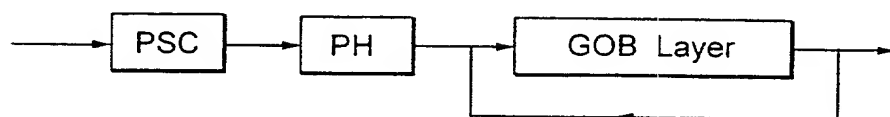
FIG. 26

SESSION LAYER
(Session Layer)



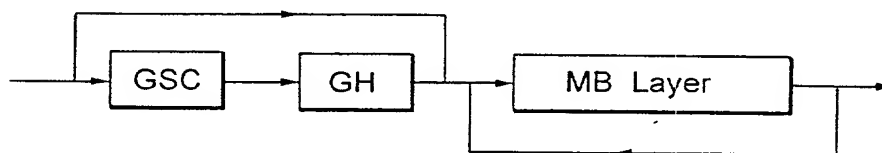
(a)

PICTURE LAYER
(Picture Layer)



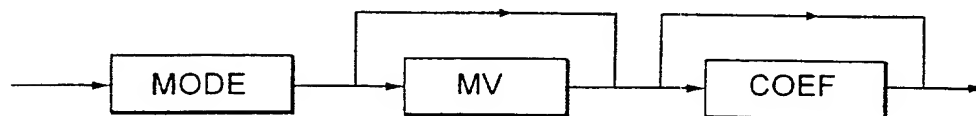
(b)

GOB LAYER
(GOB Layer)



(c)

MACRO-BLOCK LAYER
(MB Layer)



(d)

FIG.28

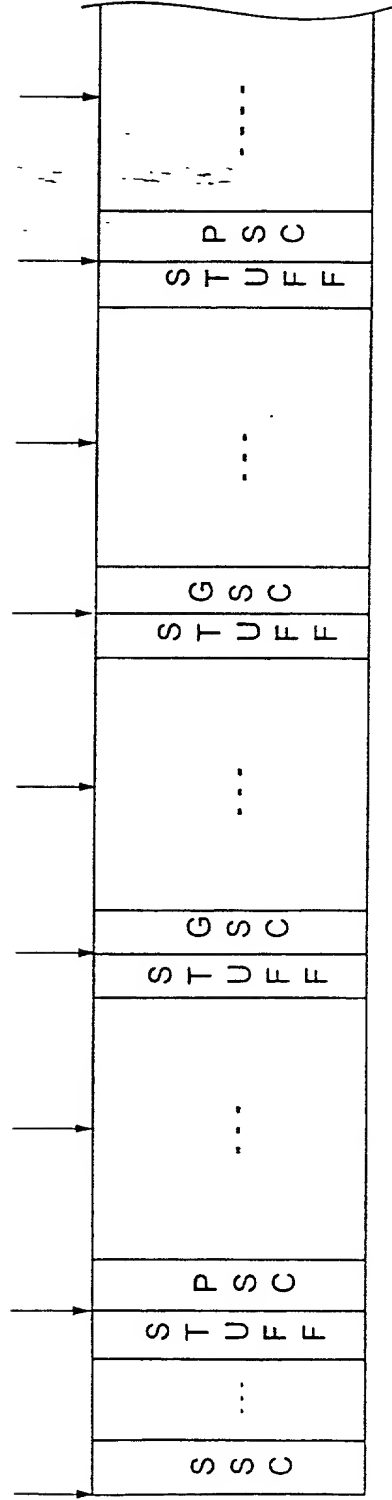


FIG. 29



FIG. 32

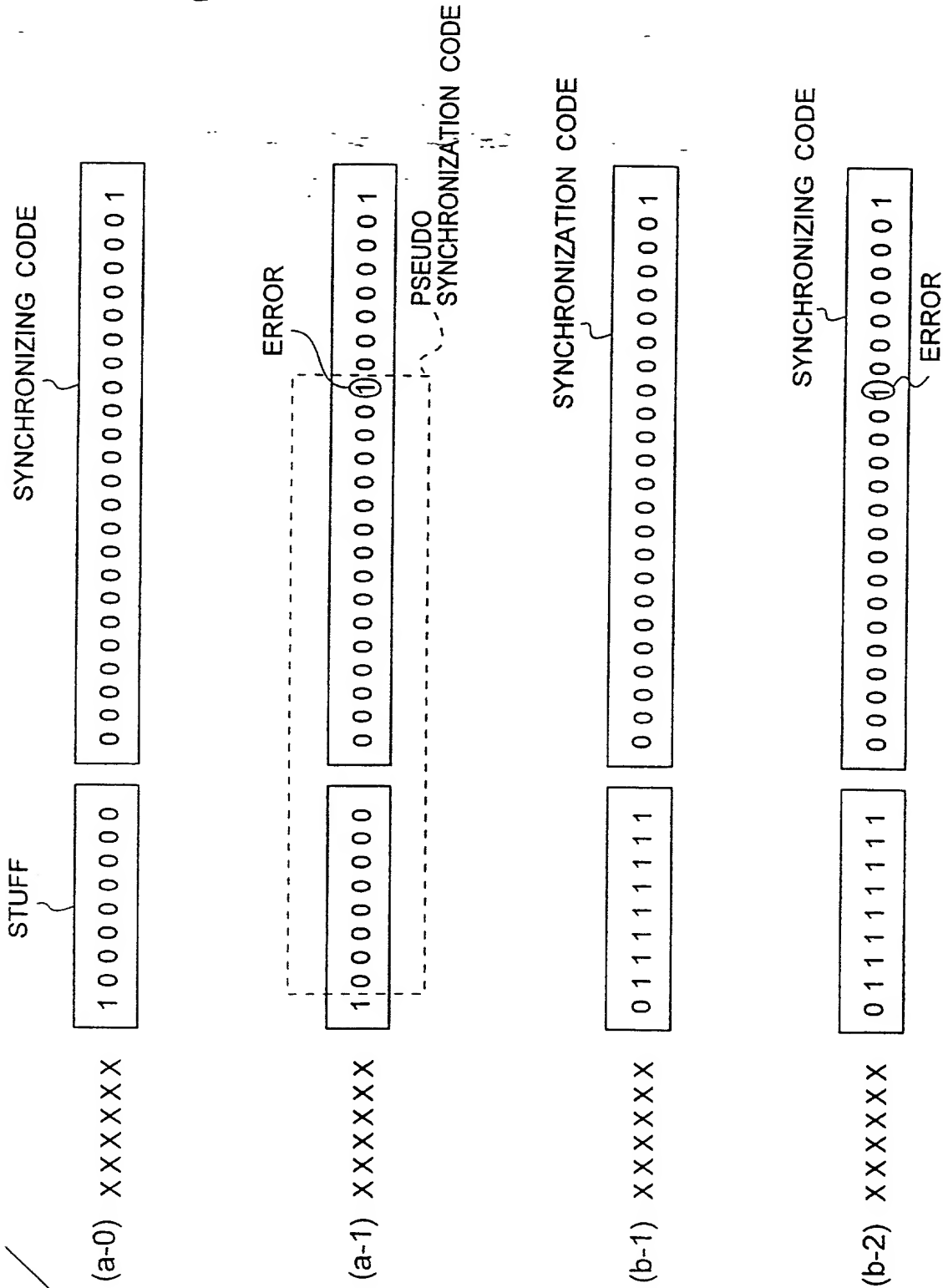


FIG. 32

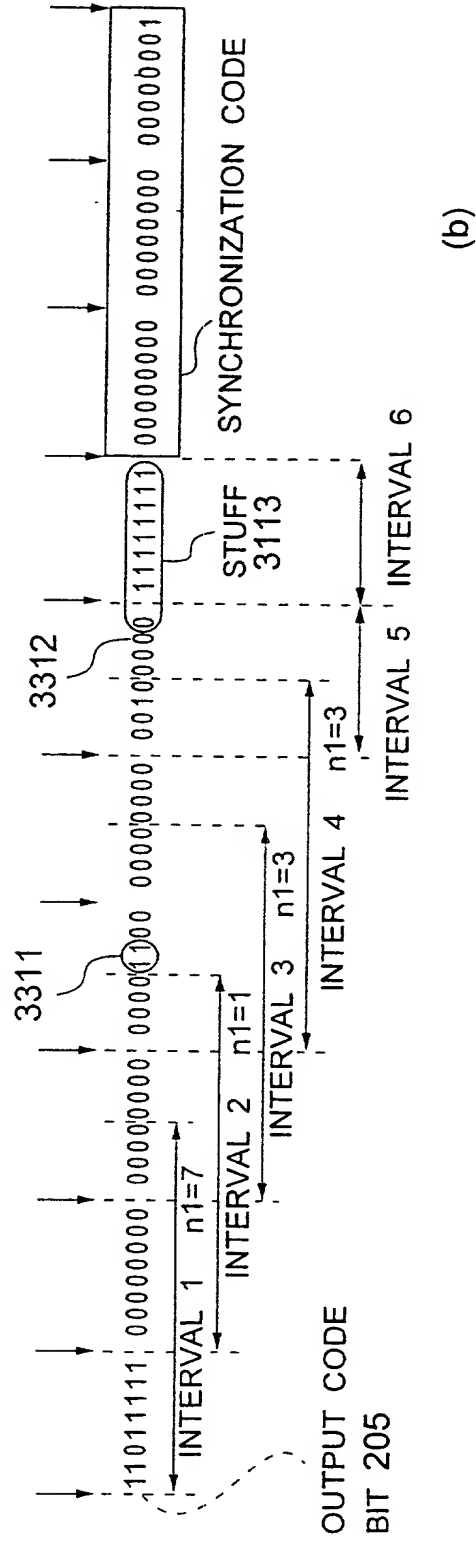
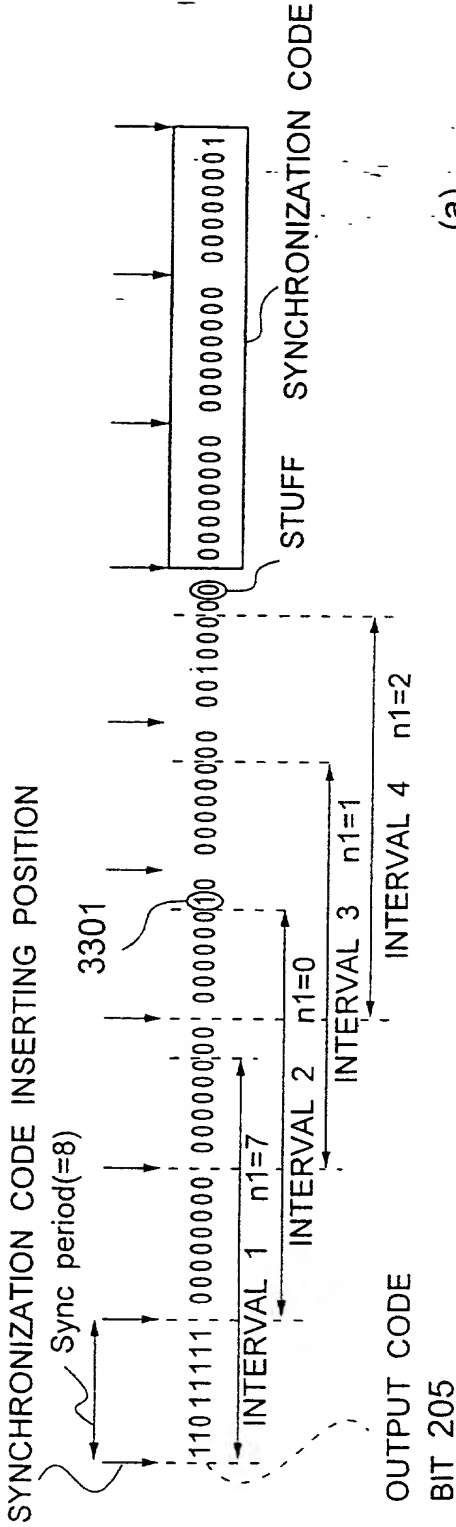


FIG. 33

SSC 00000000 00000000 00000001 00010101

SEC 00000000 00000000 00000001 00101010

PSC 00000000 00000000 00000001 00000000

GSC 00000000 00000000 1

(a)

PSC 00000000 00000000 00000001 00000000

GSC 00000001 00000000 00000000 0011

3401 GSC 3413 3412 3402

3411 3414

(b)

FIG.34

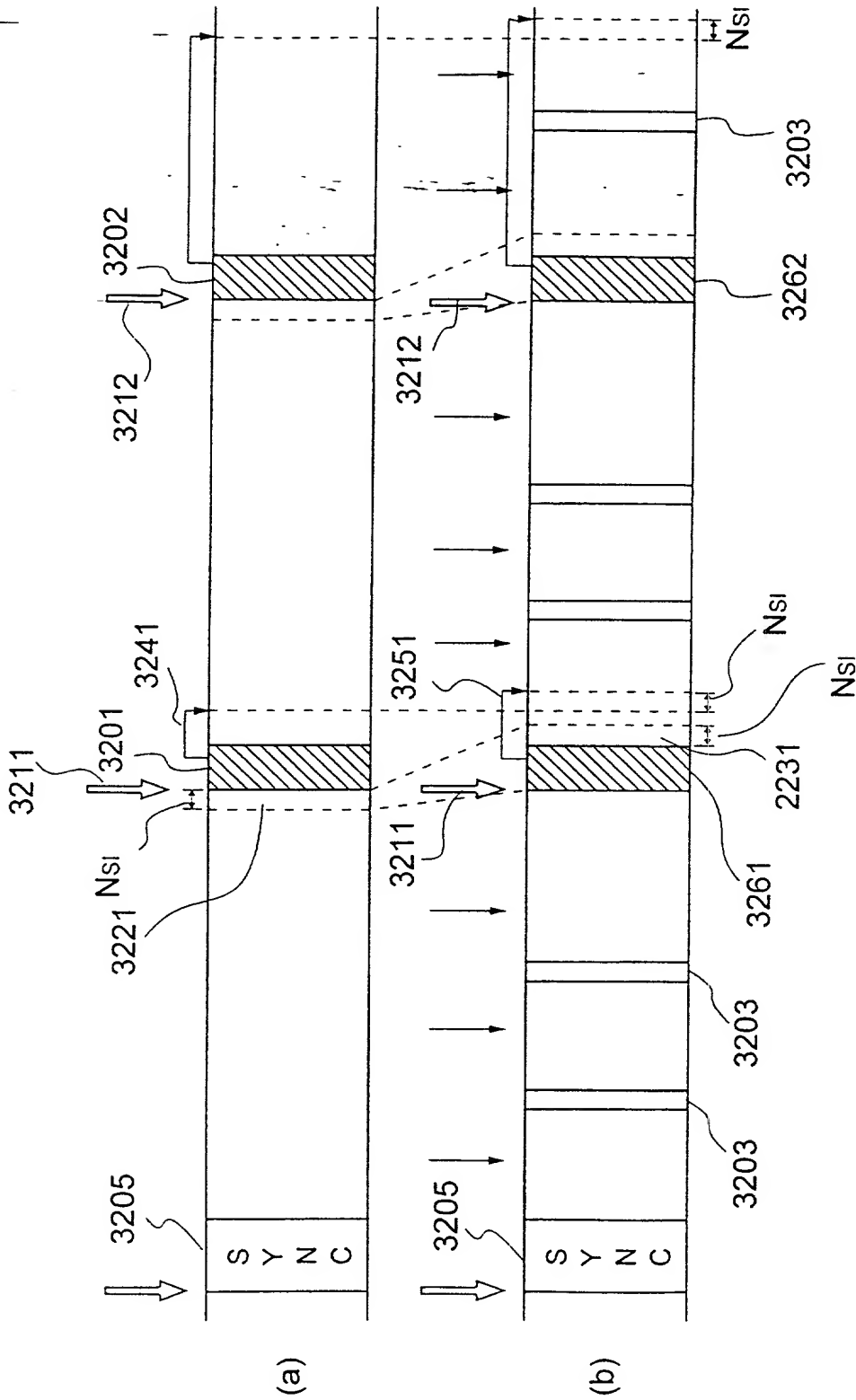


FIG. 35